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**QDR™ SRAM CONSORTIUM RELEASES SPECIFICATIONS**

*High-Bandwidth Networking SRAM Specifications Are Now Available*

**SAN JOSE, Calif., February 16, 2000** - The QDR™ SRAM Consortium, consisting of Cypress Semiconductor (NYSE: CY); IDT (Nasdaq: IDTI); and Micron Technology Inc. (NYSE: MU), today announced the completion of specifications for the first products based on the new SRAM standard for future high-performance communications applications. Datasheets for the first Quad Data Rate SRAMs are now publicly available at the QDR web site, [www.QDRSRAM.com](http://www.QDRSRAM.com), as well as each company's web site. First silicon has been probed and customer samples are scheduled for initial shipment within the next quarter. The first two versions of QDR SRAMs are 8 Mbit, organized as 512k x 18. One version utilizes a two-word burst and is ideal for small, very high performance data buffers. The other version utilizes a four word-burst and is ideal for large buffer applications.

QDR SRAMs target emerging systems, such as next generation switches and routers, operating at data rates beyond 200 MHz. The first QDR products will be capable of performance up to 333 MHz data rates. In order to assure customers have multiple, compatible sources, the QDR Consortium defined initial roadmaps and migration paths spanning to the 128-Mbit density, as well as product innovations for future bandwidth improvements. The group also standardized on the space-saving, cost-effective 13-mm x 15-mm fBGA package. To create true multiple sources quickly in the marketplace, the consortium partnered beyond simple data sheet compliance by exchanging design simulations, test vectors, test methodologies, characterization plans and common packaging support. The consortium will continue this close partnership into silicon verification and qualification.

**About QDR RAMs** Demand is growing rapidly for memories optimized for high bandwidth. The relentlessly expanding amount of information travelling over the Internet is creating the need for more and faster systems capable of routing and switching data across the globe. Higher-bandwidth memory is a requirement for these systems, and the QDR standard is specifically designed to address this need.

The development of the unique QDR SRAM architecture included extensive input from networking industry leaders. The devices are designed to greatly increase memory bandwidth

compared to existing SRAM solutions in applications such as switches and routers, and will typically be used for look-up tables, linked lists and controller buffer memory.

Targeting emerging systems operating beyond 200 MHz data rates, QDR SRAMs have dedicated input and output ports that independently run at double data rate (DDR), resulting in four data items per clock cycle. Depending upon the application, the QDR SRAMs more than double the SRAM bandwidth. The dedicated input and output ports free designers from grappling with bus contention issues.

Each vendor designs and manufactures the devices in its own technology and fabrication facilities, and will deliver products according to its own internal development schedules. All anticipate product availability in 2000. More information on the QDR SRAM technology is available at [www.qdrsram.com](http://www.qdrsram.com).

**About Cypress** Cypress Semiconductor provides high-performance integrated circuit solutions "By Engineers. For engineers." for fast-growing companies in fast-growing markets, including data communications, telecommunications, computation, consumer products, and industrial-control. With a focus on emerging communications applications, Cypress's product lines include networking-optimized and micropower static RAMs; high-bandwidth multi-port and FIFO memories; high-density programmable logic devices; timing technology for PCs and other digital systems; and controllers for Universal Serial Bus (USB). Cypress is No. 1 in the USB and clock chip markets. Its shares are listed on the New York Stock Exchange under the symbol CY and its worldwide web site is [www.cypress.com](http://www.cypress.com).

**About IDT** IDT enables a digitally connected world by providing semiconductor solutions to leading-edge designers in communications. IDT's broad product mix consists of communications memories, networking devices, RISC microprocessors, high-speed SRAMs and high-performance logic and clock management products. IDT stock is traded on the Nasdaq stock market under the symbol "IDTI." Additional information about IDT is easily accessible through the Web at [www.idt.com](http://www.idt.com) and CD-ROM by calling 800/345-7015.

**About Micron** Micron Technology, Inc., and its subsidiaries manufacture and market DRAMs, very fast SRAMs, Flash, other semiconductor components, memory modules, and personal computer systems. Micron's common stock is traded on the New York Stock Exchange (NYSE) under the symbol, MU, and its web site is [www.micron.com](http://www.micron.com).

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"Safe Harbor" Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are "forward-looking statements" involving risks and uncertainties. Please refer to the companies' Securities and Exchange Commission filings for a discussion of such risks. QDR SRAMs and Quad Data Rate comprise a new family of products developed by Cypress Semiconductor, IDT and Micron Technology.

